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William W. Gaver, John Bowers, Andrew Boucher, Hans Gellerson, Sarah Pennington, Albrecht Schmidt, Anthony Steed, Nicholas Villars, Brendan Walker
April 2004 **Extended abstracts of the 2004 conference on Human factors and computing systems**
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The Drift Table is an electronic coffee table that displays slowly moving aerial photography controlled by the distribution of weight on its surface. It was designed to investigate our ideas about how technologies for the home could support ludic activities-that is, activities motivated by curiosity, exploration, and reflection rather than externally-defined tasks. The many design choices we made, for example to block or disguise utilitarian functionality, helped to articulate our emerging under ...

Keywords: ethnography/ethnographic studies, industrial design, interaction design, multidisciplinary design / interdisciplinary design, product design

2   Simulation: A multi-agent system for the quantitative simulation of biological networks
Salim Khan, Ravi Makkena, Foster McGeary, Keith Decker, William Gillis, Carl Schmidt
July 2003 **Proceedings of the second international joint conference on Autonomous agents and multiagent systems**
Full text available: 📄 pdf(154.41 KB)   Additional Information: full citation, abstract, references, index terms

We apply the multi-agent system (MAS) platform to the task of biological network simulation. In this paper, we describe the simulation of signal transduction (ST) networks using the DECAF [9] MAS architecture. Unlike previous approaches that relied on systems of differential equations (DE), the distributed framework of MAS scales well and allows us to model large, highly interconnected ST pathways. This scalability is achieved by adopting a hybrid strategy that factors macro-level measures, such ...

Keywords: multi-agent systems, simulation

3   SPAID: software prefetching in pointer- and call-intensive environments
Mikko H. Lipasti, William J. Schmidt, Steven R. Kunkel, Robert R. Roediger
December 1995 **Proceedings of the 28th annual international symposium on Microarchitecture**
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Lipasti, M.H.; Schmidt, W.J.; Kunkel, S.R.; Roediger, R.R.;
Microarchitecture, 1995. Proceedings of the 28th Annual International Symposium on , 29 Nov.-1 Dec. 1995
Pages:231 - 236

[Abstract]   [PDF Full-Text (724KB)]   **IEEE CNF**

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2 **Errata for circuit size relative to pseudorandom oracles**
Lutz, J.H.; Schmidt, W.J.;
Structure in Complexity Theory Conference, 1991., Proceedings of the Sixth Annual , 30 June-3 July 1991
Pages:392

[Abstract]   [PDF Full-Text (56KB)]   **IEEE CNF**

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3 **Circuit size relative to pseudorandom oracles**
Lutz, J.H.; Schmidt, W.J.;
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Le-Chun Wu, Rajiv Mirani, Harish Patil, Bruce Olsen, Wen-mei W. Hwu
May 1999 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 1999 conference on Programming language design and implementation**, Volume 34 Issue 5

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With an increasing number of executable binaries generated by optimizing compilers today, providing a clear and correct source-level debugger for programmers to debug optimized code has become a necessity. In this paper, a new framework for debugging globally optimized code is proposed. This framework consists of a new code location mapping scheme, a data location tracking scheme, and an emulation-based forward recovery model. By taking over the control early and emulating instructions selective ...

2 Practical data breakpoints: design and implementation
Robert Wahbe, Steven Lucco, Susan L. Graham
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A data breakpoint associates debugging actions with programmer-specified conditions on the memory state of an executing program. Data breakpoints provide a means for discovering program bugs that are tedious or impossible to isolate using control breakpoints alone. In practice, programmers rarely use data breakpoints, because they are either unimplemented or prohibitively slow in available debugging software. In this paper, we present the design and implementation of a practical data breakp ...

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Robert Wahbe
September 1992 **ACM SIGPLAN Notices , Proceedings of the fifth international conference on Architectural support for programming languages and operating systems**, Volume 27 Issue 9

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4 The benefits and costs of DyC's run-time optimizations
Brian Grant, Markus Mock, Matthai Philipose, Craig Chambers, Susan J. Eggers
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With an increasing number of executable binaries generated by optimizing compilers today, providing a clear and correct source-level debugger for programmers to debug optimized code has become a necessity. In this paper, a new framework for debugging globally optimized code is proposed. This framework consists of a new code location mapping scheme, a data location tracking scheme, and an emulation-based forward recovery model. By taking over the control early and emulating instructions selective ...

2 <u>Debugging of globally optimized programs using data flow analysis</u>
Roland Wismüller
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Advanced processor and machine architectures need optimizing compilers to be efficiently programmed in high level languages. Therefore the need for source level debuggers that can handle optimized programs is rising. One difficulty in debugging optimized code arises from the problem to determine the values of source code variables. To ensure correct debugger behaviour with optimized programs, the debugger not only has to determine the variable's storage location or associated register. It m ...

3 <u>The benefits and costs of DyC's run-time optimizations</u>
Brian Grant, Markus Mock, Matthai Philipose, Craig Chambers, Susan J. Eggers
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DyC selectively dynamically compiles programs during their execution, utilizing the run-time-computed values of variables and data structures to apply optimizations that are based on partial evaluation. The dynamic optimizations are preplanned at static compile time in order to reduce their run-time cost; we call this staging. DyC's staged optimizations include (1) an advanced binding-time analysis that supports polyvariant specialization (enabling both single-way and multi ...

Keywords: dynamic compilation, specialization